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Textil - Provningsmetoder För Fibertyger - Del 2 ...Fibertyger - Del 2: Bestämning Av Tjocklek (ISO 9073-2:1 995) Europastandarden EN ISO 9073-2:1996 Gäller Som Svensk Standard. Detta Dokument Innehåller Den Officiella Engelska Versionen Av EN ISO 9073-2: 1996. Standarden Ersätter SS-EN 29073-2. Motsvarigheten Och Aktualiteten I Svensk Standard Till De Publikationer Som Omnämns I Denna Stan- 6th, 2021  
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Working Paper No. 597, 2003 - IFN, Institutet För ...# We Are Grateful To Per Johansson, Erik Mellander, Harald Niklasson And Seminar Participants At IFAU And IUI For Helpful Comments. Financial Support From The Institute Of Labour Market Policy Evaluation (IFAU) And Marianne And Marcus Wallenbergs Stiftelse Is Gratefully Acknowledged. \* Corresponding Author. IUI, Box 5501, SE-114 85 ... 6th, 2021.  
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